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Title A2B interface		
Size: Tabloid	Number: AB0371	Revision: 1
Date: 5/8/2024	Time: 12:02:41 AM Sheet 1 of 3	
File: C:\Users\lamab\AppData\Local\Temp\Releases\Snapshot\1\AB0371_A2B-IO.SchDoc		



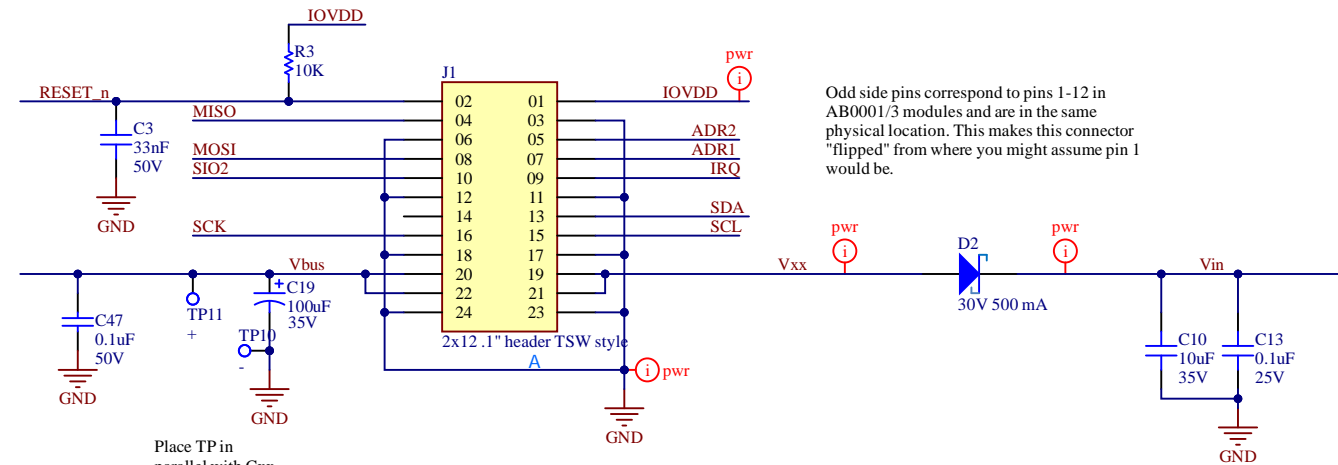
I2C pullups must be provided by the carrier that the module is plugged in to.

IOVDD 20 mA max, can be 3.3 or 1.9V (see datasheet). Default is to jumper for 3.3. LED is also powered by 3.3, but set to draw < 2 mA.

The module is intended to be a locally powered node it's assumed IOVDD isn't needed to provide power to the main board.

Vxx can be from 4.7 to 9V. 5V is typical but as each slave node in series get 0.4V less (diode drop) the # of powered slave nodes would be limited.

J1, J2 mount to the bottom side.



Odd side pins correspond to pins 1-12 in AB0001/3 modules and are in the same physical location. This makes this connector "flipped" from where you might assume pin 1 would be.

Place TP in parallel with Cxx so a TH cap can be stuffed. 2mm lead space

A2B breakout/dropin A2B subsystem using AD2437. Power is supplied by the board that this one plugs in to. This board can provide CFG-4 power for downstream nodes, and can be used with boards that would have RJ-45 or XLR based A2B with power systems.

If phantom bus power is used to power the node this board is plugged in to it must be extracted on the carrier for this board.

External power input:
Vxx: 4-9VDC for AD2437 power < 300 mA
Vbus_in: 24 VDC (max) for CFG4 A2B bus power systems (< 2A)

IOVDD can be 3.3 or 1.8V, see AD2437 for rules for use.

