Analog	Input and output connectors the same except as noted	
	Connector boards only have an input *or* output and follow that connector's convention	
pin	signal	notes
1	+15	VA+ regulated, 300 mA
2	-15	VA- regulated, 300 mA
-	GND	
4	+33	VD regulated 1A
		VD Togulatou, TA
5	GND	Connected on in and out on both OC drive, pulled up on carrier carrier may
6	RESETn	allow RESETn on out connector to jumper to MUTEn input on next module
7	SDA	I2C data (on both in and & out), pulled up on carrier
8	SCL	I2C clock (on both in and & out), pulled up on carrier
		Mute (input). Interupt (OC) on output on AA modules. On DA modules. MUTEn
9	MUTEn / INTn	out
10	GND	
11	1 (+)	
12	1 (-)	- pins are drounded on SE output. NC on SE input
12		
13		
14	2 (+)	
15	2 (-)	
16	GND	
17	3 (+)	
18	3 (-)	
19	GND	
20	4 (+)	
21	4 (-)	
22	GND	
23	5 (+)	
24	5 (-)	
25	GND	
26	6 (+)	
27	6 (-)	
28	GND	
29	7 (+)	
30	7 (-) GND	
32	8 (+)	
33	8 (-)	
34	GND	
	- D	
	-5 VA	-5 analog 200 mA
2	GND	
3	+5 VA	+5 analog 200 mA
4	GND	
5	+5VD	+5 digital 1 A
6	GND	10 shortes source
/ Q	+48 VA ±\/ΔD\//R	+46 phantom power for nower amos 12 to 35 V typical limited by current/nin of 34
0 9	GND	
10	-VAPWR	for power amps -12 to -35 V typical, limited by current/pin of 3A