

Carrier control .1" dual row

relative to carrier

pin	signal	direction	notes
1	+5	out	regulated, 1A "Digital", Off in standby
2	+3.3STDBY	out	regulated, 1A, Active in standby
3	GND		
4	GND		
5	SDA	i/o	I2C data
6	SCL	i/o	I2C clock
7	INTn	out	I2C INT req input
8	GND		
9	MUTEOUTn	in	Output (DACs) normal mute - is OR'd with other mute sources
10	OUTOFFn	in	Hard output mute (Analog output) - is OR'd with other hmute sources
11	FAULTn	out	Fault from carrier
12	GND		
13	DIGPOWERn	in (OC)	Output to place primary power in standby mode
14	ANAPOWERn	in (OC)	Output to enable analog supplies
15	RESETn	in (OC)	Reset entire carrier
16	GND		
17	PWRGOODn	out	Power good from (digital) supply
18	SYNC	i/o	System sync reference
19	GND		
20	TC	i/o	Time Code

+5V provided in case USB A port is needed on controller device

read I2C to find source

SPI Connector 10 pin dual row .1"

1	MOSI	in	SPI Master out
2	MISO	out	SPI Master in
3	GND		
4	SCK	out	SPI clock
5	GND		
6	SSEL0	out	Slave Select 0
7	SSEL1	out	Slave Select 1
8	SSEL2	out	Slave Select 2
			Slave Select 3
9	SSEL3	out	
10	GND		

Test/AUX I2C 6 pin .1" single row polarized

1	GND		
2	SDA	data	
3	GND		
4	SCL	clock	
5	VCC3V3	+3.3V	NC if 1.8V system
6	RESETn		